

# SMART Memory Test Lab Report



MOTHERBOARD	MANUFACTURER	CHIPSET	TEST NUMBER	TEST STATUS
S5520SCR	INTEL	INTEL 5520	SMTL0038	PASS

### Module Information

Manufacturer Name:	SMART MODULAR TECH	Module Type:	DDR3 REGISTERED ECC DIMM
Manufacturer P/N:	SG5127RD325893HB	CAS Latency:	C999
Memory Capacity:	4GB	Assembly Type:	SINGLE BOARD NON STACKED
Module Configuration:	6 PER CPU /12 TOTAL	Low Profile:	NO
DRAM Supplier:	HYNIX	Register P/N:	IDT IDTSSTE32882-HLBAKG
DRAM Part Number:	H5TQ2G83BFR-H9C	PLL P/N:	N/A
PCB Part Number:	PH5AG240NEBHB2RB A	Dual Die Package:	
PCB Layer Count:	6	Comment:	

### MINIMUM SYSTEM INFORMATION

System Name:	S5520SCR
Serial Number:	QSHV00800136
BIOS Version:	AMI S5500.86B.01.00.0045
Processor Code:	INTEL XEON X5650
OS Memory Size:	24GB
OS Version:	LINUX SMP 2.6.28.10
Test Version:	HPL XLINPAK 10.2.3 MKL
Comment:	

### MAXIMUM SYSTEM INFORMATION

System Name:	S5520SCR
Serial Number:	QSHV00800136
BIOS Version:	AMI S5500.86B.01.00.0045
Processor Code:	INTEL XEON X5650
OS Memory Size:	48GB
OS Version:	LINUX SMP 2.6.28.10
Test Version:	HPL XLINPAK 10.2.3.MKL
Comment:	

### PRE-TESTING INFORMATION

Board Revision:	
Insertion Test:	PASS
SPD Check:	PASS
DDR Speed Check:	PASS
Boot Time:	1 MIN

### Voltage Margining Info

Volt %	+/-3%
Volt Low	1.45 volt
Volt High	1.55 volt

### TESTING DETAILS FOR VOLT LOW

Temperature:	55C	Time:	12HRS
Start Date:	8/25/2010		
Stop Date:	8/30/2010		
HPL Test Status:	PASS		
DDR Speed:	1333MHz		
Summary Min:	P		
Summary Max:	P		

Test Status:	PASS
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### TESTING DETAILS FOR VOLT HIGH

Temperature:	55C	Time:	12HRS
Start Date:	8/26/2010		
Stop Date:	8/31/2010		
HPL Test Status:	PASS		
DDR Speed:	1333MHz		
Summary Min:	P		
Summary Max:	P		

Test Status:	PASS
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### Serial Number List

1) SFR2010080600005	2) SFR2010080600004	3) SFR2010080600008	4) SFR2010080600010
5) SFR2010080600003	6) SFR2010080600002	7) SFR2010080600001	8) SFR2010080600009
9) SFR2010080600012	10) SFR2010080600007	11) SFR2010080600011	12) SFR2010080600006
13)	14)	15)	16)

### CONTACT INFORMATION

Contact: Arthur Sainio Phone: 1-510-624-8126 e-mail: [arthur.sainio@smartm.com](mailto:arthur.sainio@smartm.com)

Test Traveler Notes:

VERIFICATION

Verified by: Jim Schwarz

Test Technician: Mark Durand

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