

SMART CMM-E3S

The SMART CMM-E3S is a memory module utilizing the CXL[®] 2.0 standard in an E3.S form factor. The integrated controller supports a PCIe Gen5 x16 CXL interface and 64GB or 96GB of DDR5 DRAM.

Compute Express Link (CXL) is an open industry interconnect standard for providing high bandwidth and low latency. It adds connectivity between host processors, accelerators, memory devices, and network interface cards (NICs). CXL also enables memory expansion and acceleration across high-performance computational workloads for AI, ML, HPC, and communications segments.

SMART develops CXL Type 3 (CXL.mem) memory products to address the industry's need for more memory per processor core. The CXL interconnect specification allows for a significant expansion of memory form factor options leveraged from SSDs.

Key Features

- Available in EDSFF E3.S 2T (2U short) form-factor
- CXL-2.0 compatible with PCIe-Gen5 speeds running at 32GT/s
- 64GB, 96GB densities
- Supports Reliability, Availability and Serviceability (RAS) features added in CXL-2.0
- Powered by only 12V supply from EDSFF compatible edge interface (SFF-TA-1009)
- Supports sideband interfaces for real-time debug, management and system update, enabling out-of-band management of the module
- Supports additional security features to protect data from side-channel attacks



Applications

- For serial-attached memory expansion needed in data-intensive computing applications
- Adds tiered cache for memory

Ordering Information

CMM-E3S Memory Module	
SMART Part Number	Description
STXAB64GMBF4S40SB	64GB Memory modules in E3.S 2T form-factor with dual ranks/channel DDR5, utilizing CXL 2.0
STXAB96GMBF4S40MB	96GB Memory modules in E3.S 2T form-factor with dual ranks/channel DDR5, utilizing CXL 2.0

Contact: Torry Steed at torry.steed@smartm.com for any sales and sampling queries.



For more information, please visit: www.smartm.com

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