

SMART Memory Test Lab

SMART Memory Test Lab (SMTL)

Extensive system-level memory validation testing has been shown to be very effective. It is typically done on an OEM or off-the-shelf system with specific sets of memory modules. System-level memory validation testing can help OEMs save money by eliminating problems and reducing the time to market.

SMART offers ATE and system memory qualification services for OEM and industry-standard systems across a wide variety of applications. Detailed validation reports are available on SMART's website. SMART's Memory Test Lab is outfitted with state-of-the-art equipment for environmental testing, functional testing and signal quality analysis. Its memory experts perform thorough system-level analysis and debug to determine root-cause effects.

SMTL testing is offered as a free service for SMART customers with test reports posted to www.smartm.com/developerstoolbox and validation reports sent directly to customers for OEM-specific boards.



System Memory Validation/Qualification Services for:

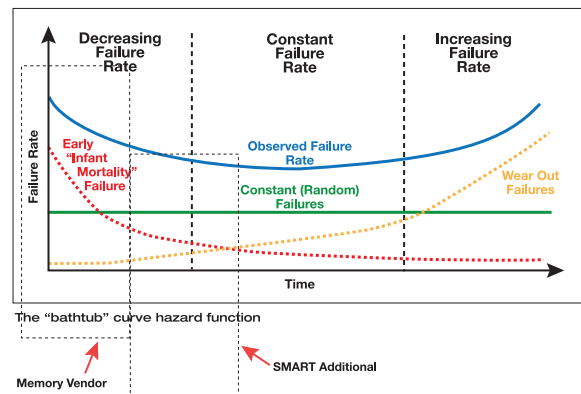
- Intel DDR3 server boards
- OEM-specific boards
- Supply voltage reduced from 1.8V to 1.5V

Broad Range of System Testing

- Voltage margining
- Heavy/light loading
- HPL-modified software
- BIOS change support

Testing Details

- Two corners voltage/temperature -12 hours each/24 hours at 55°C
- -/+3% of P1 and P2 V5-CPU1 [1.45v & 1.55v] at 55°C
- Power cycling
- AC or DC, 50 cycles at each test corner
- Test min (half the slots) and max configuration (all slots)
- Testing RDIMMs at 1333MHz
- Test software - Intel Xeon 64-bit optimized HPL



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